

PixQuanta Test Engineer Vacancy, March 2021

PixQuanta is seeking to hire a Test Engineer to help further the development of its leading edge proprietary photosensor technology. The candidate will lead in the design, build and verification of a benchtop test setup to verify PixQuanta's sensors in 3D imaging performance, including the optimal design for the sensor package. The candidate will also be involved in the wafer-level test of PixQuanta's sensors and manage the flow from wafer test to device packaging.

Initially the candidate will evaluate and improve upon an existing 3D imaging benchtop setup in at the Tyndall Institute. This will lead to an improved setup to accurately test the performance of PixQuanta's 3D imaging technology. As a follow on project, a portable 3D imaging test bench will be designed and built to demonstrate 3D imaging performance. This system will be used in a real-life environment.

Throughout the period the candidate will be evaluating sensors at die and part level as part of the engineering development effort.

Location: Cork city, Ireland.

Reports to: The Test Engineer will report to PixQuanta CEO.

About PixQuanta: PixQuanta is a start-up technology company seeking to grow rapidly and develop its exciting proprietary photosensor technology for the growing sensor market. PixQuanta seeks to hire diversely and create a challenging and rewarding environment for independent and motivated engineers and scientists. Employees will be offered a variety of benefits including participation in the Employee Stock Ownership Plan.

Responsibilities and Duties:

- Carry out wafer level testing of PixQuanta sensors.
- Continue the development of existing 3D imaging test benches for evaluation of PixQuanta sensors.
- Evaluate, test and choose packages for PixQuanta's sensors and manage the package supply chain through 3rd parties, including wafer dice, and/or backgrind, and die packaging.
- Reporting of test results and scheduling.

Required experience

- Electro-Optical testing of discrete components.
- Wafer probe testing and using a semiconductor analyser.
- Experience with analogue circuit design at PCB level, with optical receivers experience a plus.

Qualifications

- Engineering or physics degree at Master's level or higher.
- Familiarity with silicon CMOS devices.
- Familiarity with high-speed RF techniques.
- Must be dynamic, motivated, creative thinker who can work independently and in small teams, and available to travel when required.
- Excellent communication skills.