

PixQuanta Test Engineer Vacancy, April 2020

Reports to: The test engineer will report to PixQuanta CEO or CTO.

PixQuanta is seeking to hire a test engineer to help further the development of its leading edge photosensor technology. The candidate will lead in the design, build and verification of a benchtop test setup to verify PixQuanta's sensors in LiDAR (Light Detection and Ranging) performance, including the optimal design for the PixQuanta sensor package. The candidate will also be involved in the wafer-level test of PixQuanta's sensors and manage the evaluation of dies prior to packaging. In the first 3 months the candidate will evaluate and improve an existing LiDAR setup. In the first 6 months an improved LiDAR setup will be created. In one year, a portable LiDAR test bench will be designed and built to demonstrate 3D imaging performance. Throughout the period the candidate will be evaluating sensors at die level as part of the development effort in conjunction with the process engineer responsible.

Location: Cork city, Ireland.

About PixQuanta: PixQuanta is a start-up hardware technology company seeking to grow rapidly and develop its exciting photosensor technology for the growing sensor market. With a fresh company there comes the opportunity to form an environment where you can thrive. PixQuanta seeks to hire diversely and create a challenging and rewarding environment for independent and self-motivated engineers and scientists.

Responsibilities and Duties:

- Develop LiDAR test benches for evaluation of PixQuanta sensors.
- Conduct wafer level testing of PixQuanta sensors.
- Evaluate, test and choose packages for PixQuanta's sensors and manage the package supply chain through 3rd parties, including wafer dice, and/or backgrind, and die packaging.
- Conducting package level qualification and test.
- Conducting functional test such as LiDAR or imaging.
- Reporting of test results and scheduling.
- Managing and scheduling of environmental testing such as HTOL/ and radiation testing

Required experience

- Electro-Optical testing of discrete components.
- Analogue circuit design.
- Experience with RF circuit design (in particular for optical receivers).
- Wafer probe testing and using a semiconductor analyser.
- Good working knowledge of diode laser technologies in the visible-SWIR wavelength range.
- Good knowledge of imaging technologies.

Qualifications

- Engineering or physics degree at Master's level or higher.
- Must be familiar with silicon CMOS devices.
- Must be familiar with high-speed RF techniques.
- Must be dynamic, motivated, creative thinker who can work independently and in small teams, and available to travel when required.
- Must have excellent communication skills.